

FIG.3

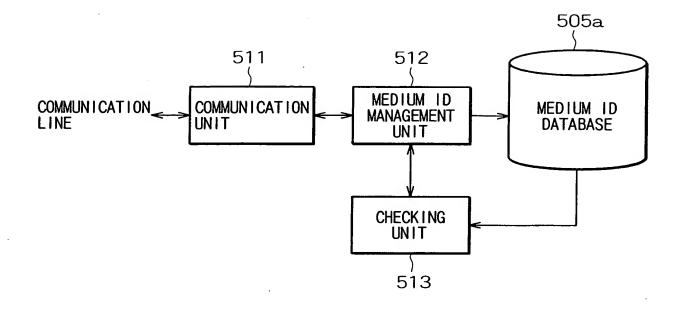
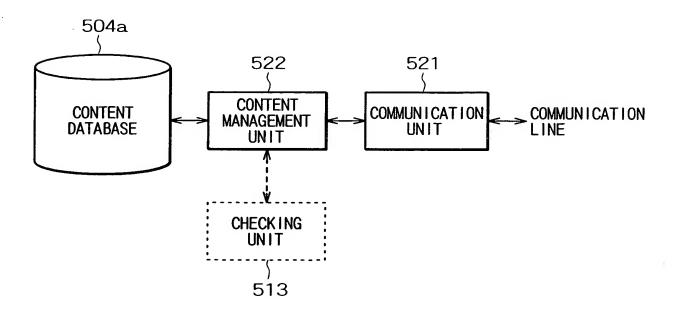
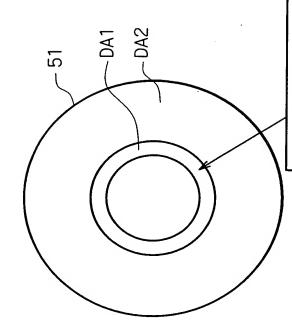


FIG.4



F1G.5A

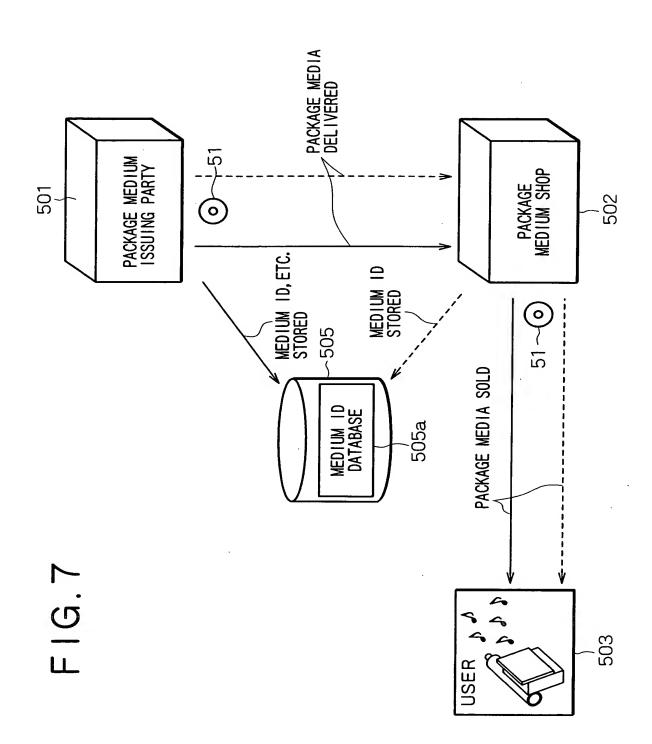


F1G.5B

MED	MEDIUM ID	ACCES	ACCESS RIGHT INFORMATION	MATION
Q I	PASSWORD	CONTENT	UPLOAD	DOWNLOAD
10001A	qwerty	access Key	access Key 0 (AUTHORIZED) 0 (AUTHORIZED)	0 (AUTHORIZED)

## F1G.6

MEDIL	MEDIUM I D	ACCESS RIGHT INFORMATION	INFORMATION	
۵ì	PASSWORD	CONTENT	UPLOAD	DOWNLOAD
10001A	qwerty	/media/id/1001A/access Key 0(AUTHORIZED)	0 (AUTHORIZED)	0 (AUTHORIZED)
10002B	yuiop@	/media/id/1002B/access Key	1 (UNAUTHORIZED) 0 (AUTHORIZED)	0 (AUTHORIZED)
10003C	asdfg	/media/id/1003C/access Key 1 (UNAUTHORIZED) 0 (AUTHORIZED)	1 (UNAUTHORIZED)	0 (AUTHORIZED)
10004A	zxcvbn	/media/id/1004D/access Key 1 (UNAUTHORIZED) 0 (AUTHORIZED)	1 (UNAUTHORIZED)	0 (AUTHORIZED)
	}-	))	<b>}</b>	
X60006	i"#\$%&'	/media/id/9009X/access Key 1 (UNAUTHORIZED) 0 (AUTHORIZED)	1 (UNAUTHORIZED)	0 (AUTHORIZED)



#### FIG.8

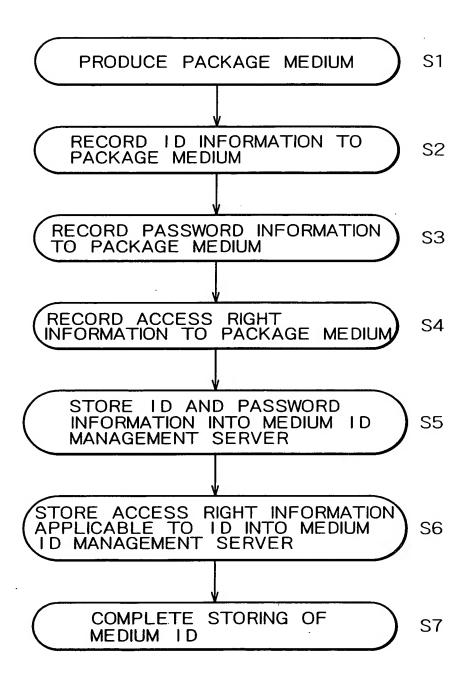
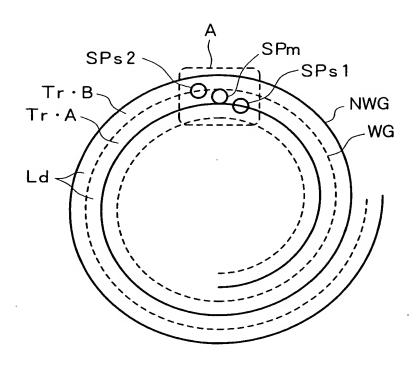
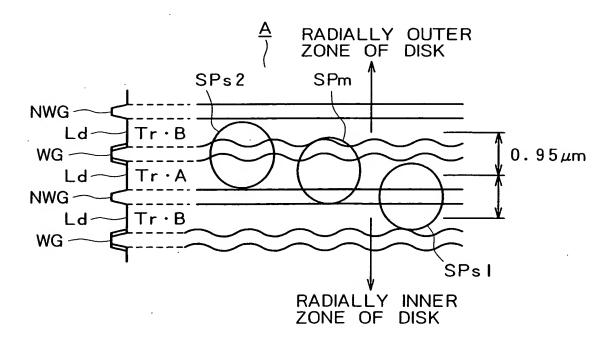


FIG.9



### FIG. 10A FIG. 10B



# looeylor.leeool

# F1G.11

	MD-DATA2	MD-DATA1
TRACK PITCH	0.95 µm	1.6 µm
BIT LENGTH	$0.39 \mu m/bit$	0.59 µm/bit
γ·NA	650nm · 0.52	780nm · 0.45
RECORDING MODE	LAND RECORDING	GROOVE RECORDING
ADDRESS SYSTEM	INTERLACE ADDRESSING (DOUBLE-SPIRAL ONE-SIDE WOBBLE)	SINGLE-SPIRAL TWO-SIDE WOBBLE
MODULATION TECHNIQUE	RLL (1,7)	EFM
ERROR CORRECTING SYSTEM	RS-PC	ACIRC
INTERLEAVE	BLOCK CONTAINED	CONVOLUTION
REDUNDANCY	1 9. 7%	46.3%
LINEAR VELOCITY	2.0m/s	1.2m/s
DATA RATE	589kB/s	133kB/s
STORAGE CAPACITY	650MB	1 4 0MB

FIG. 12A

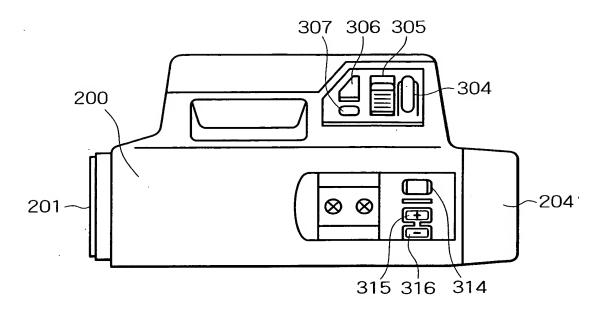
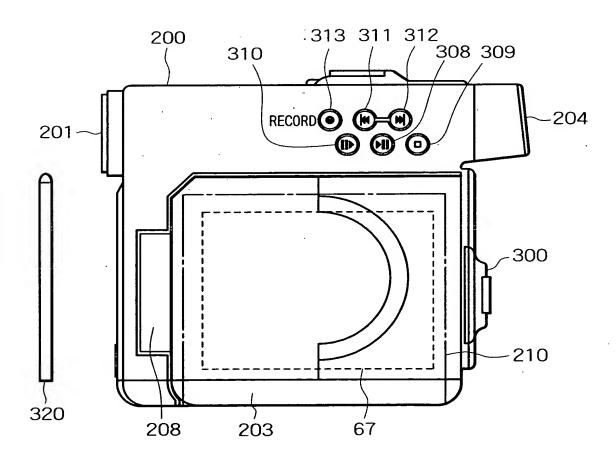


FIG. 12B



~ 300 - 301 , 206 302 ) ERASE 200 PS1 FIG. 13B PS2 PS3 PS4 303b 303 203 ~ 204 FORWARD DIRECTION REVERSE --DIRECTION 303a~ - 201 F1G.13A 202,205 R STEREO ( 200

FIG. 14A

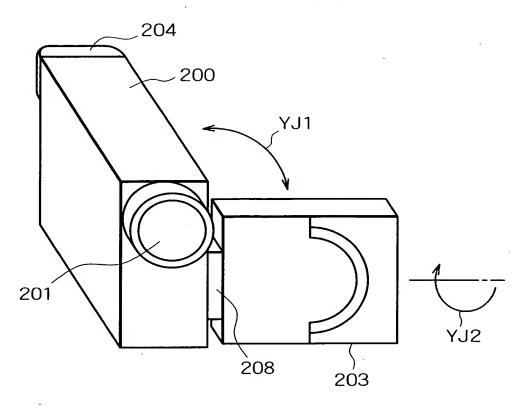
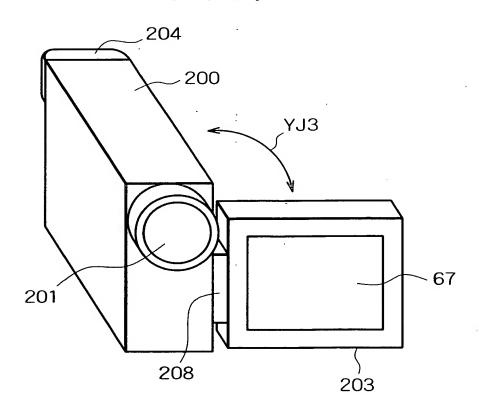


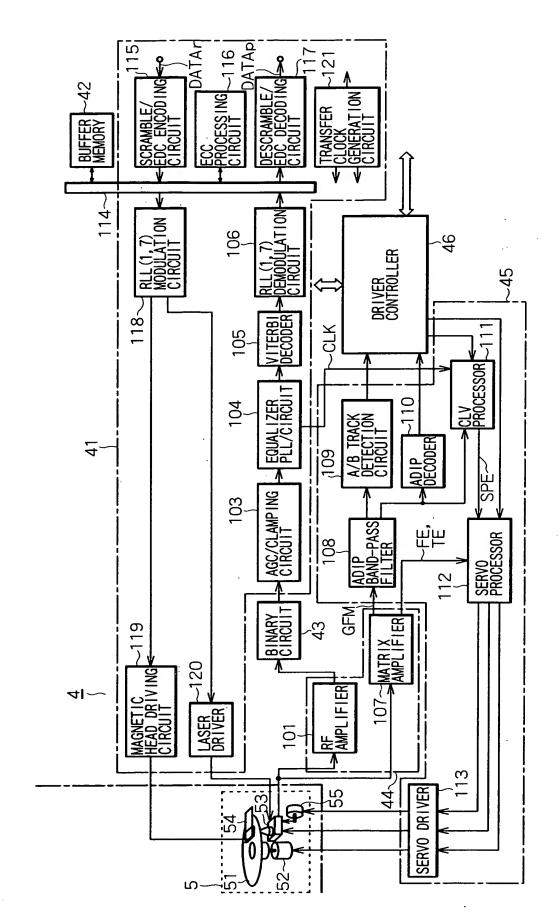
FIG. 14B



207 VIEW FINDER 204 გ ) ~45 ~46 ~47 POWER SUPPLY BLOCK DRIVER CONTROLLER MD-DATA1 ENCODER/DECODER MD-DATA2 ENCODER/DECODER SERVO CIRCUIT VIEW FINDER DRIVE UNIT BUFFER MEMORY 437 OPERATION UNIT ~32 ~37 38 ~34 DATA PROCESSING/SYSTEM CONTROL CIRCUIT MEMORY VIDEO CONTROLLER BUFFER MEMORY 39 MEMORY -63 ,64 7 2 2 25 55 A/D CONVERTER VIDEO D/A CONVERTER EXTERNAL INTERFACE 6 CAMERA CONTROLLER TIMING GENERATOR , 62 T3 - C1 VIDEO 68|6A~

FIG. 15

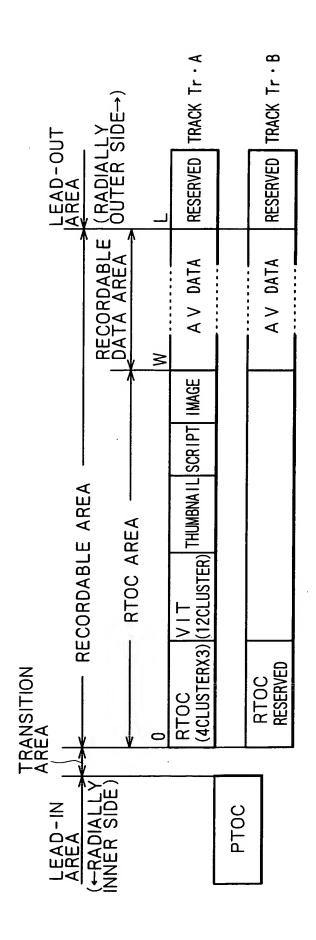
F1G.16



## FIG. 17

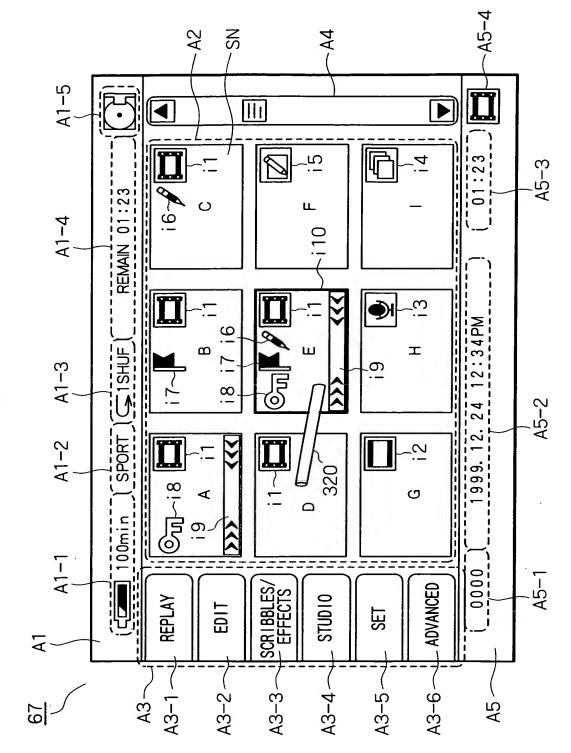
PTOC RTOC	
VOLUME INDEX TRACK	
THUMBNAIL PICTURE TRACK	
TRACK (FILE)	
AUXILIARY DATA TRACK (INCLUDING SCRIPT, IMAGE FILE, ETC. )	

F1G. 18

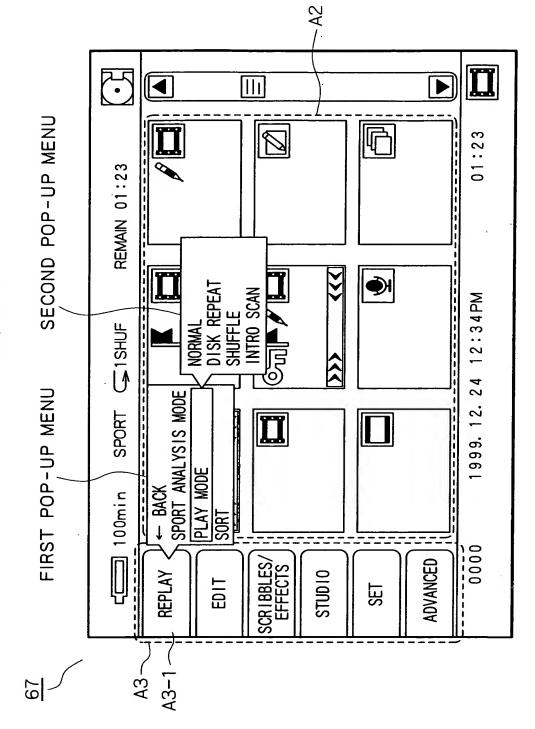


W:RECORDABLE DATA AREA START ADDRESS L:LEAD-OUT AREA START ADDRESS

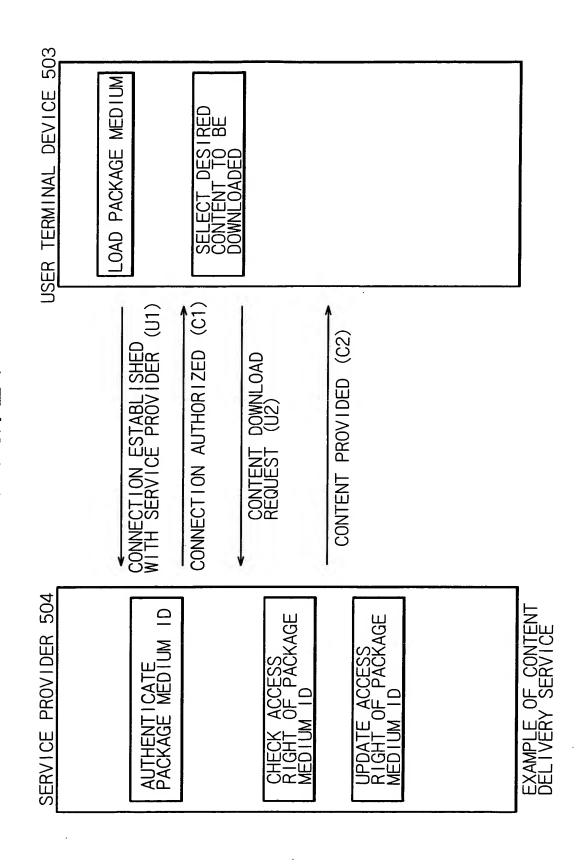
F1G.19



F1G.20



F1G.21



## F1G.22

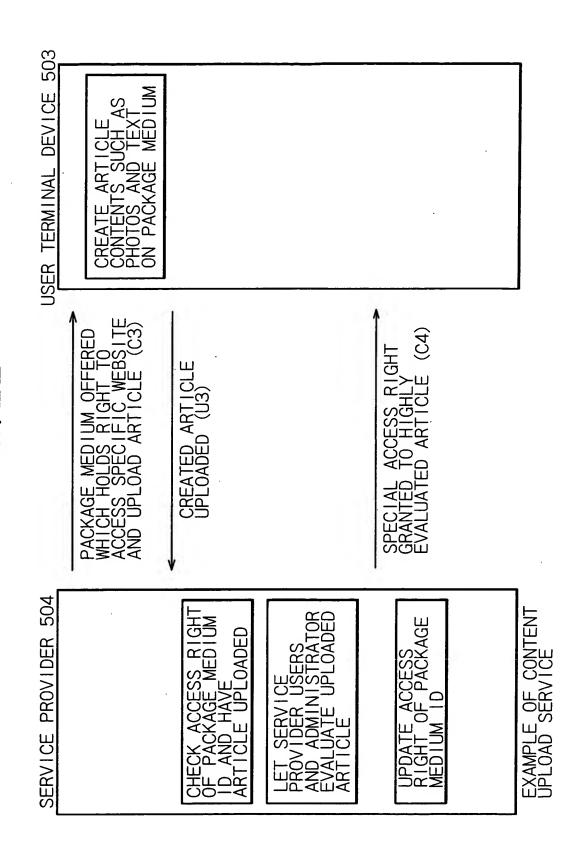
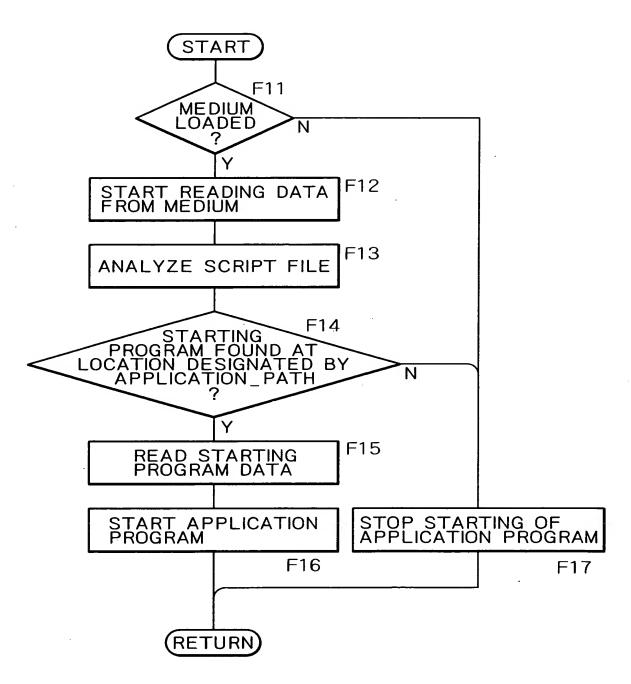


FIG. 23



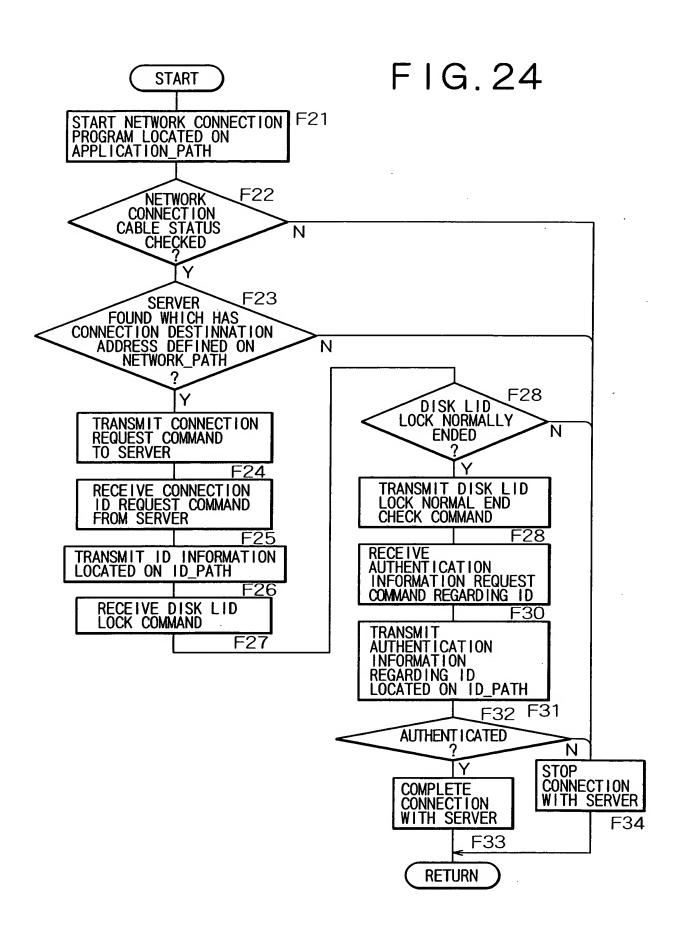
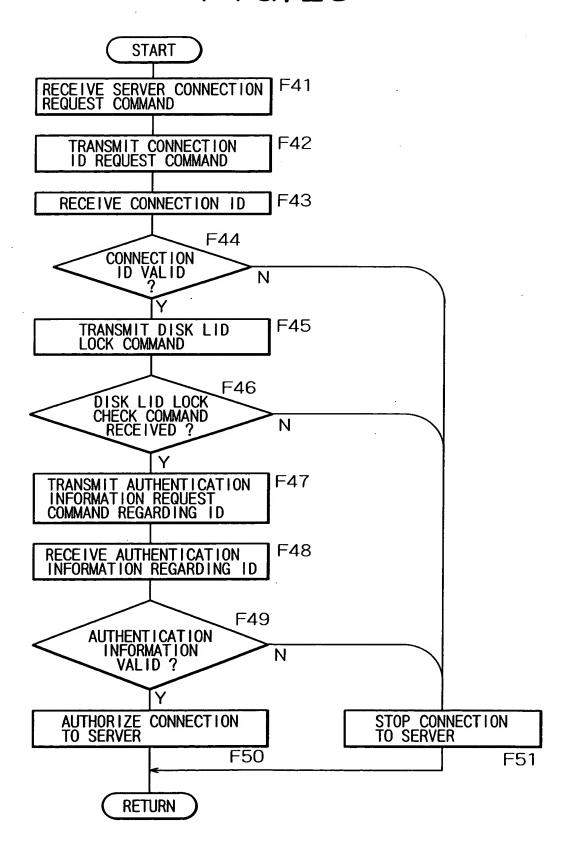
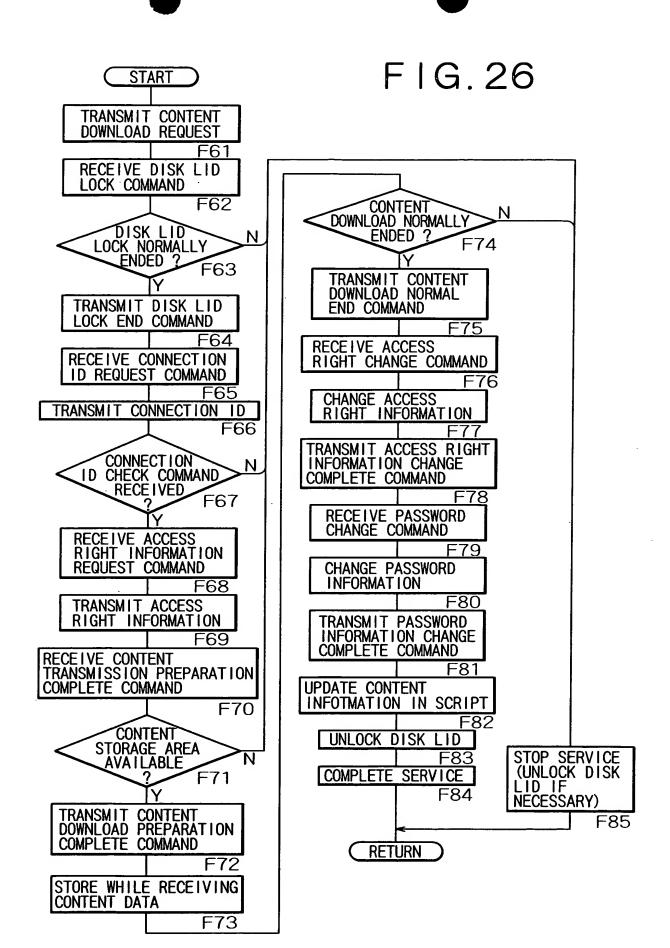
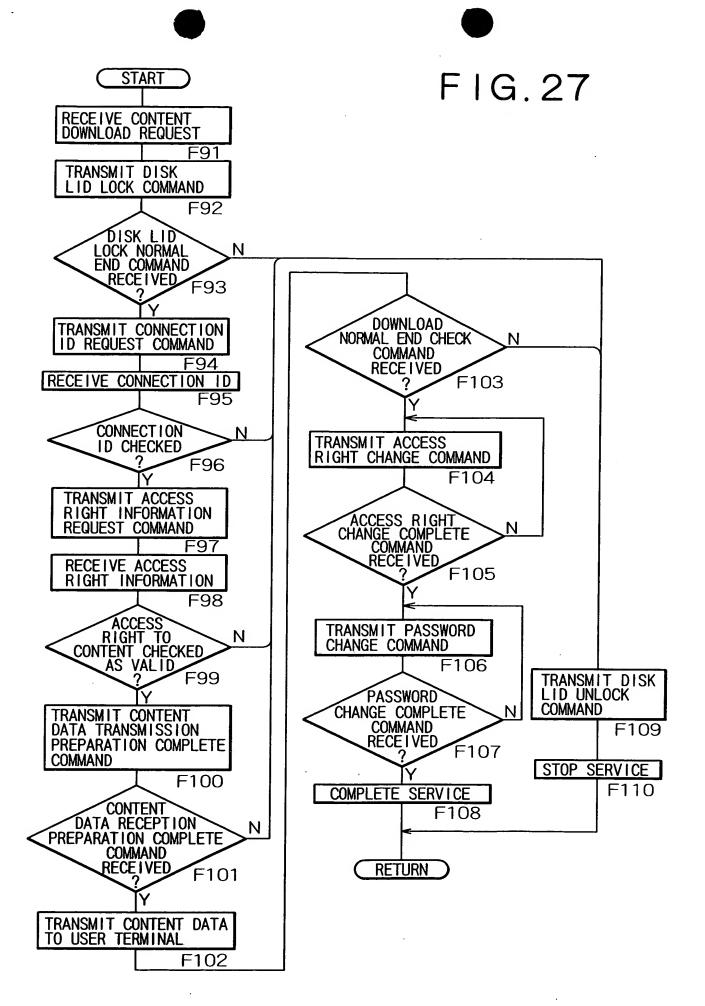


FIG. 25







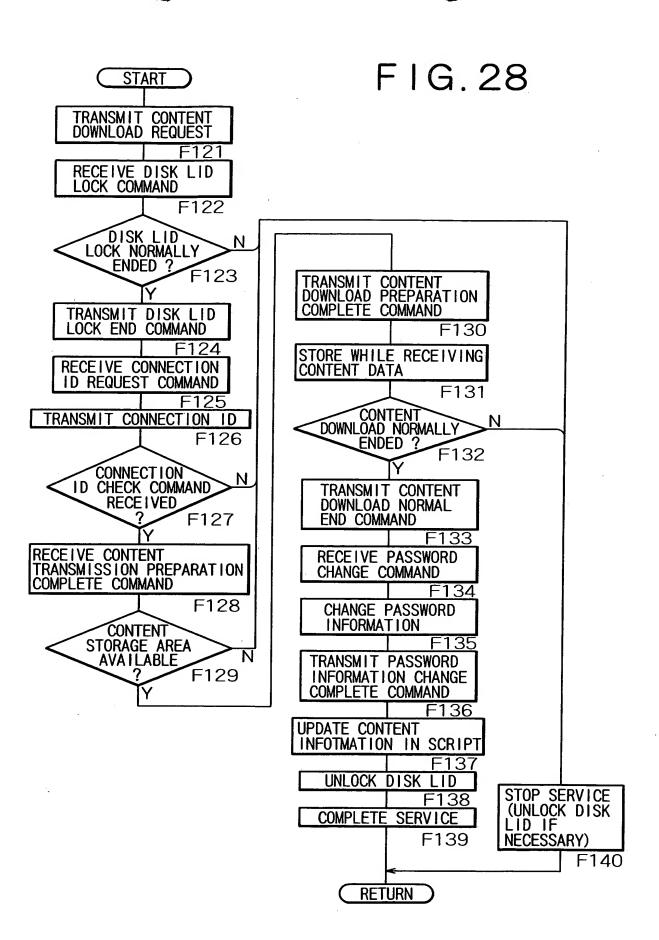
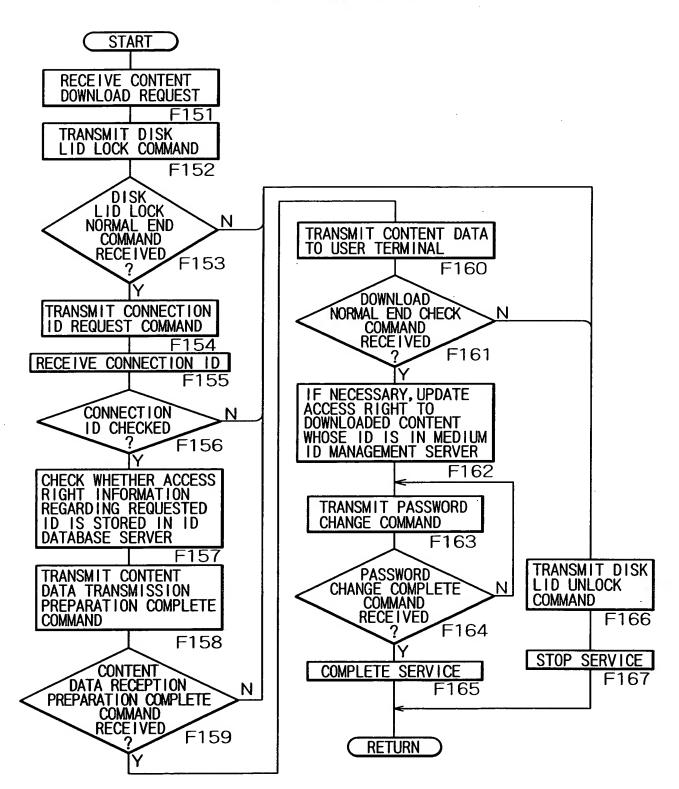
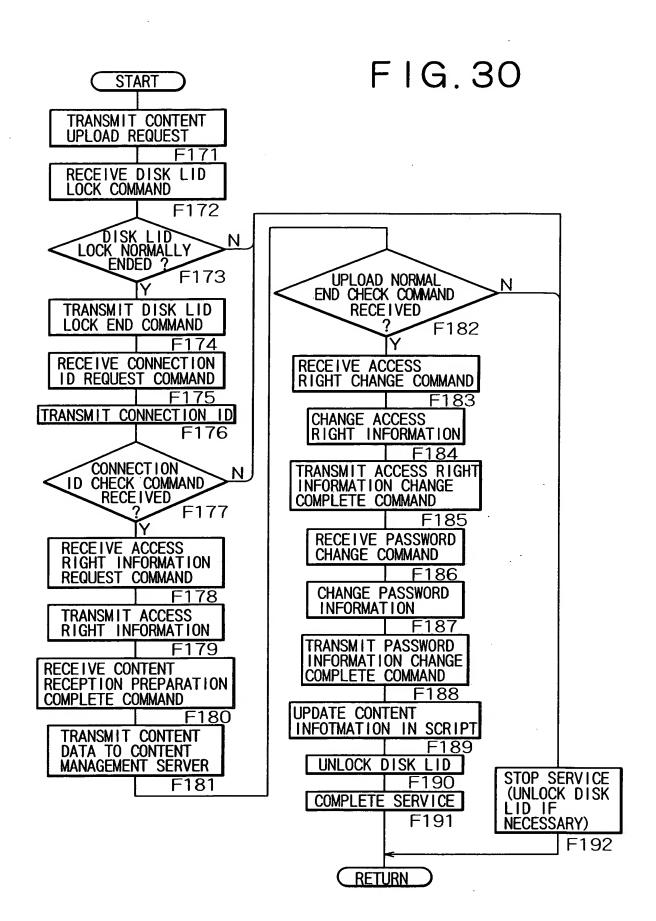
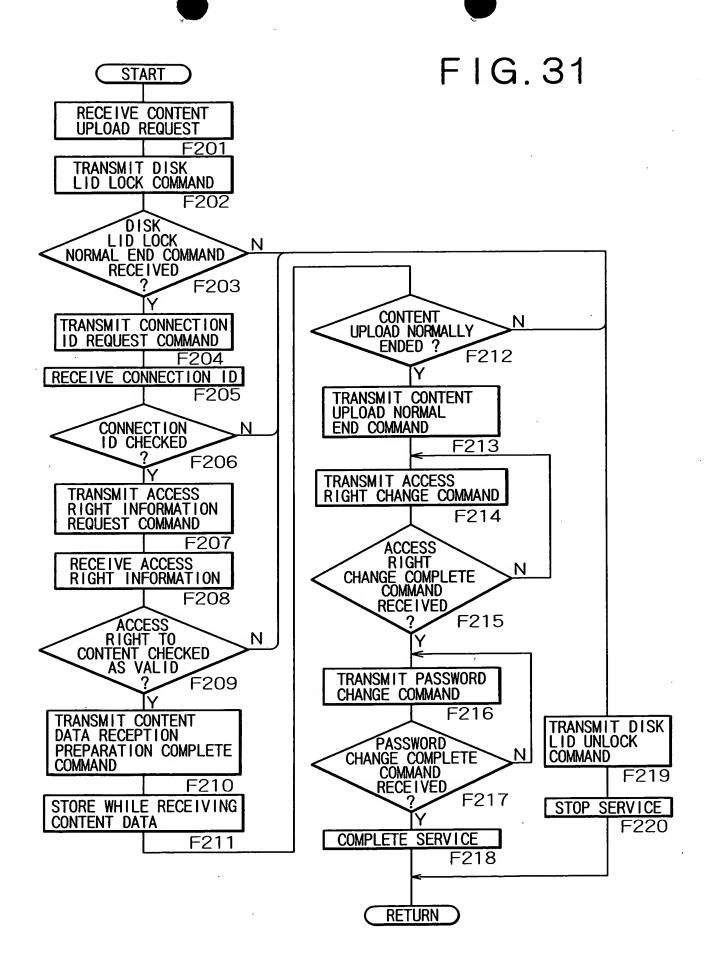
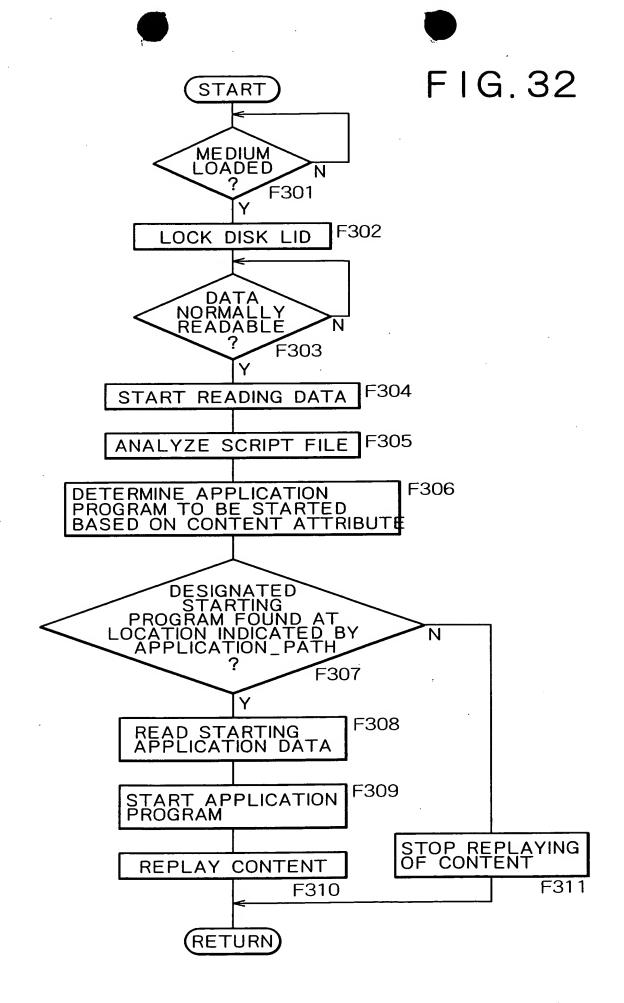


FIG. 29









€3

ABLE 405 HARD DISK 409 DRIVE INPUT/OUTPUT INTERFACE F1G.33 404 RAM 408 N PULL 407 403 ROM 406 CPU